

## Robert Boguski Bio

1981 graduate of The University of San Francisco (BA Economics). Robert has worked in the electronics packaging and interconnect manufacturing and testing business, primarily in management positions, since he was a teenager (1975). 22 years in the bare board business, 12 years in the EMS business, and 14 years in the testing and test engineering business (Some overlap with EMS). Together with his wife Regina, Robert is the Owner and President of Datest, a test engineering and failure analysis company located in Fremont, California. Robert and/or his Company, Datest, are members of SMTA, IPC, and IEEE. Datest is also a member of the American Society for Nondestructive Testing (ASNT) as well as the European Institute of Printed Circuits (EIPC). Since 2013 Robert has co-authored the Test and Measurement column for *Circuits Assembly* Magazine. Robert served from 2013-2016 as the Vice-President, Technical Programs, and from 2016-2019 as the President of the SMTA Silicon Valley Chapter. He is currently a Member of the Global Board of Directors of SMTA.