Dynamic trends in IOT (Internet of Things), mobility, and connectivity are driving the electronics industry to innovate towards deeper integration of sophisticated components and sub-systems. Key requirements such as sub-system level test, sub system miniaturization, and, integration of IC components created from different silicon node technologies, are intensifying market interest in System-In-Package and System-in-Module technologies. The ASE Group is the world’s largest semiconductor OSAT and is leading the way in offering unique module solutions to enable the next generation of advanced products. Technical challenges associated with the manufacturing of these modules span SMT component attach to advanced shielding methods and have led to more miniaturized and product differentiating market solutions. This presentation will deliver an overview on how industry dynamics are fueling greater interest in SiP/SiM Technology while paving the way for exciting new electronic products.
Bio-

Mark is the Director of Technical Marketing at ASE-US Inc., specializing in Flip Chip and Advanced Interconnect package technologies. Mark joined ASE in Feb of 2015, and brings a diverse set of semiconductor experiences from Texas Instruments, Motorola, and Dallas Semiconductor, including advanced interconnect & IC package development, as well as a history of new product introductions. Mark holds a bachelor’s degree in Mechanical Engineering from Texas A&M University, and has worked in the Semiconductor Industry for the last 20 years, has written +20 papers, and currently holds over 30 patents in the area of semiconductor packaging. Mark is excited about the opportunity to share how ASE’s SiP technology is helping to enable a next generation of consumer electronic products.

Upcoming Chapter meetings
June Technical Speaker:

Mark Gerber of ASE Group:
“Packaging Roadmap” at our June 19th, Chapter meeting.

Chapter Calendar 2015/16:
July & August
Open

September -
Chapter meeting on the 11th
Chapter Training Program

October -
SMTAI

November -
Chapter meeting on the 13th
“Medical Electronics”

December -
Members Party on the 4th

January—
Chapter Meeting on the 8th
“Failure Analysis”

February—
Hosting SMTA Chapter Leadership Forum
Presidents Message

Greetings to SMTA Dallas Chapter members and guests.

As we enter into the summer months, we will have our meeting in June and then we all take July & August off to due to everyone’s vacation schedule conflicts. We will have a chapter meeting and speaker in September and then focus on SMTA-I in Chicago for October.

Check the schedule for the details as we are trying to line up topics for a rolling 12 months look-ahead. Our chapter continues to invest in audio video equipment to accommodate the larger groups we are hosting. Your patience and suggestions are appreciated. We hope to see you June 19th and enjoy the rest of the summer.

Gary Tanel
Dallas Chapter President

Dallas Chapter Membership Rolls— 139 Members !!

We welcome our newest member to the SMTA family: Adam Ley of ASSET InterTech who is joining us as an Individual member

Global Members: 7, Individual Members: 69, Corp Members: 34,
Participating Members: 19, Associate/Student Members: 9

Dallas Chapter Volunteers

“Many Hands Make Light Work” - THANKS !

Jimmy Fallin—Meeting setup and Audio
Alicia Hamby—Web site and Linkedin updates

Carol Primdahl—Name Badges
Tiao Zhou—Technical Committee

Dallas Chapter Corporate Member List - June 2015

ACD Fujiitsu Network Communications Precision Technology, Inc.
Barry Sales, Inc. GSC (Garland Service Company) Roper Resources, Inc.
BBM, Inc. JTAG Technologies Southwest Systems Technology Inc.
Circuitronics Keysight Technologies SPEA America
Conecsus LLC Krypton Solutions Sunshine Global PCB Group
CVInc. Mek Americas LLC SVTronics, Inc.
DG Marketing Corporation Morgan Newton Company The IPS Group, LLC
Dragon Circuits National Circuit Assembly Trilogy Circuits, Inc.
E.T.S. Group, Inc. NPI Technologies, Inc. Variosystems, Inc.
Electrolab Inc. One Source Group VI Technology
EWD Solutions PAC Global, Inc.
FHP Reps Philips Entertainment
Page 4 - Just wondering…

Are you reading this at work?

Here's a morning caffeine quiz.

1. You're an average American coffee drinker. That means you consume ...
   a) 1 cup a day b) 2 cups a day c) 3 cups a day

2. True or false: If coffee says "decaffeinated" on the label, you can assume there's no caffeine in it.
   9. Roughly what percentage of the world's adults drink coffee every day?

3. How long does it take to get the peak caffeine rush from a cup of coffee?
   a) 5 minutes b) 15 minutes c) 30 minutes

4. True or False: You get more caffeine in a cup of espresso than in a mug of coffee.

5. • True or false: The way you can tell how much caffeine is in coffee beans is how dark or light they are.

6. Do people get more sensitive or less sensitive to caffeine as they get older?

7. • True or false: Coffee is the world's second most popular drink.

8. On average, how long will it take your body to get rid of the first half of the caffeine you've drunk?
   a) 2 to 3 hours b) 3 to 6 hours

10. Who rids their bodies of caffeine quicker - men or women?

11. True or false: Drinking moderate amounts of coffee can reduce your chance of getting colon cancer by as much as 30%.

12. Where does most of the caffeine used in soft drinks come from?
   a) The kola nuts used in flavoring b) Decaffeinated coffee c) A lab in Omaha, Nebraska

13. Why is caffeine put in soft drinks?

14. True or false: Drinks containing caffeine make you urinate more.

Answers on Page 6
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Page 5 Technology Corner

Typical net-list issues that fabricators run into.

By far the most common net-list issue is a designed in AGND to DGND short. Usually the first net assigned in the IPC net-list (net 0 or net1). A simple note on the read me or even the header of the IPC file itself describing a “Known” AGND to DGND short would preclude any phone calls from the fabricator.

Other common Net-list issues are DEFINITION based. When Generating IPC net-lists sometimes erroneous points are called out on the IPC net-list. We have seen fiducials, targets and non-plated holes all defined as net-points by mistake in the past. The fabricator should be able to select the net point being called the open and highlight the area. Many times this is where it is found that a “broken” or open net may indeed be a definition issue pointing to a board fiducial or a non-plated hole.

Ground shorts are a little more difficult to show. Sometimes if we attempt to query a specific GROUND short on a plane layer the system simply displays the ENTIRE plane making it difficult if not impossible for the fabricator to be able to tell precisely where the GND short resides. This is why many times fabricators simply send the net-list report generated by their CAM system and ask the End user to tell them if they are safe to proceed or if they are legitimate design issues.

Remembering once again the purpose of the net-list compare is to compare the customers' supplied IPC net-list (design iteration) against the customers' provided GERBER data.

Lastly, sometimes net-list mis-matches occur because things that may be present once the part is assembled or in its canister. Things like Non plated holes with large ground pads intended to make a connection with a screw head in its later life will manifest themselves as “broken” or Open nets when the net-list compare is performed, that is because the screws that make the ground connection are not present at the board fab stage.

Similarly, Castellated holes or plated half holes at the part edge many times come up as “broken” or open nets simply because there is no metal post to connect them to at the point the bare board is being fabricated.

Fortunately most fabricators can help point this out and continue forward with the job with little loss of time.

Karl Doebbert...
REASONS TO JOIN YOUR LOCAL SMTA CHAPTER

- Do you have something to share such as new technologies or new products?
- Are you new in a sales territory – do you know all the players? Do they know you?

Be informed and involved on the Local Chapter level:

- Take advantage of technical information provided at local meetings. Get to know colleagues in your local area.
- Network for technical information and get to know companies and people in your local area.
  - Share information with colleagues on new products and services.

THE MISSION OF SMTA

The Surface Mount Technology Association (SMTA) membership is a network of professionals who build skills, share practical experience and develop solutions in electronic assembly technologies and related business operations.

MEMBERSHIP DUES

Participating: $50 - If your company (same location/division) holds a Corporate Membership it’s employees are eligible to receive the full range of benefits at a discounted price.

Individual: $75 - This membership is designed for individuals who wish to join SMTA to receive all the benefits independent of a Corporate Membership.

Corporate: $450 - A corporate membership in SMTA provides discounts to employees located in the same location/division where the Corporate Membership is held.

Student/Retiree: $5 - The Student/Retiree Membership is available to all full-time post-secondary students and retirees at a discounted rate.

HOW TO JOIN

On-line at www.smta.org

PAGE 5 ANSWERS: 1=b, 2=False, 3=c, 4=False, 5=False, 6=More Sensitive, 7=True, 8=b, 9=About 50%, 10=men, 11=True, 12=b, 13= to addict you to the drink, 14=True.