

Phil Burlison is presently a Director of Advanced Technology at Verigy Ltd., a leading ATE company. Prior to joining Verigy Phil was the CTO of Inovys, which was acquired by Verigy at the beginning of this year. Inovys, co-founded by Phil in 1999, had a mission to develop new ATE hardware and software based on a product strategy that addresses the need for more cost effective and productive solutions for semiconductor test based on DFT and structural test methodologies. With over 40 years of experience in semiconductor design and test, Phil's key strength has been recognizing and grasping the significance of technology trends and the application of innovative design architectures to address these opportunities. Prior to founding Inovys, Phil was Director of Technology at LTX Corporation. Prior to LTX, Phil's other experience consisted of roles at ATE companies Macrodata, Fairchild/Schlumberger, GenRad, Parallel Systems, Megatest, and LTX/Trillium. At these companies, he was responsible for defining the architecture for over twelve new ATE platforms. Prior to these companies, Phil started a career in semiconductor design in 1966 at Signetics, where he worked on the design of analog IC's. Later at National Semiconductor Phil worked on the development of the first MOS memories, and also developed some of the company's initial MOS simulation software.